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UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
JOLY et al. )  
Serial No.: Not Yet Assigned ) Examiner:  
Filing Date: Herewith ) Not Yet Assigned  
For: INTEGRATED CIRCUIT ON ) Group Art Unit:  
HIGH-PERFORMANCE CHIP ) Not Yet Assigned

CLAIM FOR PRIORITY

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

The above-referenced application claims the benefit of priority to French Patent Application No. FR 0307617 filed June 24, 2003, which is incorporated by reference herein.

Respectfully submitted,

  
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December 15, 2005

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